

Access DB# 149055

SEARCH REQUEST FORM

Scientific and Technical Information Center

Requester's Full Name: Pinchus Laufer Examiner #: 73139 Date: 3/24/05
Art Unit: 2100 Phone Number 272-3599 Serial Number: 10/821,833
Mail Box Location: 1C81 Results Format Preferred (circle): PAPER DISK E-MAIL

If more than one search is submitted, please prioritize searches in order of need.

Please provide a detailed statement of the search topic, and describe as specifically as possible the subject matter to be searched. Include the elected species or structures, keywords, synonyms, acronyms, and registry numbers, and combine with the concept or utility of the invention. Define any terms that may have a special meaning. Give examples or relevant citations, authors, etc, if known. Please attach a copy of the cover sheet, pertinent claims, and abstract.

Title of Invention: _____

Inventors (please provide full names): _____

Earliest Priority Filing Date: _____

**For Sequence Searches Only* Please include all pertinent information (parent, child, divisional, or issued patent numbers) along with the appropriate serial number.*

Litigation
6,370,564

Inventor: John Bretscher

O.G. Date August 3, 2004

STAFF USE ONLY

Searcher: Shirelle Green

Searcher Phone #: 306-4767

Searcher Location: 41240

Date Searcher Picked Up: MAR 28 2005

Date Completed: _____

Searcher Prep & Review Time: _____

Clerical Prep Time: _____

Online Time: 10

Type of Search

Sequence (#) _____

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Vendors and cost where applicable

STN

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Questel/Orbit 11.58

Dr.Link _____

Lexis/Nexis

Sequence Systems

WWW/Internet

Other (specify) _____

1 of 1 DOCUMENT

UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT

6370564

[Link to Claims Section](#)

April 9, 2002

Computer system architecture and method for multi-user, real-time applications

REISSUE: April 9, 2004 - Reissue Application filed Ex. Gp.: 2152; Re. S.N. 10/821,833 (O.G. August 3, 2004)

INVENTOR: Bretscher, John - Elgin, Illinois

APPL-NO: 055523 (09)

FILED-DATE: February 12, 2001

GRANTED-DATE: April 9, 2002

ASSIGNEE-AT-ISSUE: Ameritech, Services, Inc., Hoffman Estates, Illinois, 02

ASSIGNEE-AFTER-ISSUE: April 9, 2004 - ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS)., PKT TECHNOLOGIES, LLC 195 N. HARBOR DRIVE SUITE 5403 CHICAGO ILLINOIS 60601, Reel and Frame Number: 15215/0014

CORE TERMS: user, server, dedicated, architecture, processor, front-end, modem, station, processing, network ...

ENGLISH-ABST:

An architecture is disclosed for a computer system that runs applications serving multiple users. The computer system includes multiple processors, some of which run quick applications, i.e., requiring real time response, while others run applications with less stringent requirements. Each real time processor can be dedicated to running just one instance of an application. The processors can be of disparate types running disparate operating systems and optimized for disparate applications. The system is centrally controlled with the processors communicating among themselves over a shared LAN or via a communications switch. The system may also facilitate simultaneous voice and data communications among users. Users communicate with the system using any of a number of standard techniques: including dial-up telephone lines, ISDN, packet access services, ADSL, cable TV and the like.

PARENT-PAT-INFO:

This application is a division of application Ser. No. 08/661,428, filed Jun. 11, 1996, now U.S. Pat. No. 6,175,854, filed Jan. 16, 2001.

LEXIS-NEXIS
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File: ALL

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Library: NEWS
File: CURNEWS

us6370564/pn

** SS 2: Results 1

Search statement 3

?prt full nonstop legalall

1/1 PLUSPAT - (C) QUESTEL-ORBIT- image

PN - US2001039569 A1 20011108 [US20010039569]

PN2 - US6370564 B2 20020409 [US6370564]

TI - (A1) COMPUTER SYSTEM ARCHITECTURE AND METHOD FOR MULTI-USER REAL-TIME APPLICATION

PA - (B2) AMERITECH SERVICES INC (US)

PA0 - Ameritech, Services, Inc., Hoffman Estates IL [US]

PA2 - (B2) AMERITECH SERVICES INC (US)

IN - (A1) BRETSCHER JOHN (US)

AP - US5552398 19980406 [1998US-0055523]

FD - Division of: US6175854 - 20010116

PR - US5552398 19980406 [1998US-0055523]

- US66142896 19960611 [1996US-0661428]

IC - (A1) G06F-015/16

EC - H04L-029/06

- H04L-029/06C2

ICO - T04L-029/06C8

PCL - ORIGINAL (O) : 709205000; CROSS-REFERENCE (X) : 709203000 709219000

DT - Corresponding document

CT - US4935870; US4949248; US5163131; US5392400; US5583993; US5630757;
US5671377; US5778368; US6023685; US6029175; US6044205; US6075863;
US6182109; US6192408

STG - (A1) Utility Patent Application published on or after January 2, 2001

STG2- (B2) U.S. Patent (with pre-grant pub.) after Jan. 2, 2001

AB - An architecture is disclosed for a computer system that runs applications serving multiple users. The computer system includes multiple processors, some of which run quick applications, i.e., requiring real time response, while others run applications with less stringent requirements. Each real time processor can be dedicated to running just one instance of an application. The processors can be of disparate types running disparate operating systems and optimized for disparate applications. The system is centrally controlled with the processors communicating among themselves over a shared LAN or via a communications switch. The system may also facilitate simultaneous voice and data communications among users. Users communicate with the system using any of a number of standard techniques: including dial-up telephone lines, ISDN, packet access services, ADSL, cable TV and the like.

UP - 2001-46

1/2 LGST - (C) EPO

PN - US2001039569 A1 20011108 [US20010039569]

- US6370564 B2 20020409 [US6370564]

AP - US5552398 19980406 [1998US-0055523]

ACT - 20030923 US/CC-A

CERTIFICATE OF CORRECTION

- 20040803 US/RF-A

REISSUE APPLICATION FILED

EFFECTIVE DATE: 20040409

UP - 2004-34

2/2 LGST - (C) EPO

PN - US2001039569 A1 20011108 [US20010039569]

- US6370564 B2 20020409 [US6370564]
AP - US5552398 19980406 [1998US-0055523]
ACT - 20021022 US/CC-A
CERTIFICATE OF CORRECTION
UP - 2003-22

1/1 CRXX - (C) CLAIMS/RRX
PN - 6,370,564 A 20020409 [US6370564]
PA - Ameritech Services Inc
ACT - 20040409 REASSIGNED
ASSIGNMENT OF ASSIGNORS INTEREST

Assignor: AMERITECH SERVICES, INC., DATE SIGNED: 03/29/2004

Assignee: PKT TECHNOLOGIES, LLC, 195 N. HARBOR DRIVE, SUITE 5403,
CHICAGO, ILLINOIS, 60601

Reel 015215/Frame 0014

Contact: PETER K. TRZYNA, P.O. BOX 7131, CHICAGO, IL 60680

- 20040409 REISSUE REQUESTED
ISSUE DATE OF O.G.: 20040803
REISSUE REQUEST NUMBER: 10/821833
EXAMINATION GROUP RESPONSIBLE FOR REISSUEPROCESS: 2152

Reissue Patent Number:

- 20040511 REASSIGNED
ASSIGNMENT OF ASSIGNORS INTEREST

Assignor: AMERITECH SERVICES, INC., DATE SIGNED: 03/29/2004

Assignee: PKT TECHNOLOGIES, LLC, 195 N. HARBOR DRIVE, SUITE 5403,
CHICAGO, ILLINOIS, 60601

Reel 015642/Frame 0087

Contact: PETER K. TRZNY, P.O. BOX 7131, CHICAGO, IL 60680